

2. Electrical spec

LG24061-I63C

2 4 0 X 6 4 Dots 1 / 6 4 D U T Y

ELECTRICAL CHARACTERISTICS

T_a= 25°C V_{DD}=5.0+/-0.25 v

Input "High" Voltage (V_{IH})

2.2 V min

Input "Low " Voltage (V_{IL})

0.8 V max

APPLICABLE FOR -LOP

STN

TEMPERATURE

Supply Current, (I_{DD})Typ., mA

NORMA WIDE

12 12

Supply Current, (I_{EE})Typ., mA

1 1

Supply Voltage, (V_{EE})Typ., V

-15 -15

Recommend LCD drive Voltage:

(V_{DD}-V_O)at T_a = -20°C,Volts

N/A 17.6

T_a = 0°C

16.6 16.6

T_a = 25°C

15.3 15.3

T_a = 50°C

14.1 14.1

T_a = 70°C

N/A 13.1

ABSOLUTE MAXIMUM RATINGS

NORMAL

WIDE TEMPERATURE

TEMPERATURE

Input Voltage (V_I) V

MIN MAX MIN MAX

0 V_{DD} 0 V_{DD}

Supply for Logic (V_{DD}-V_{SS}) V

0 7 0 7

Supply for LCD (V_{DD}-V_O) V

0 23 0 23

Operating Temperature T_{OP}, °C

0 +50 -20 +70

Storage Temperature T_{ST}, °C

-20 +70 -30 +80

OPTION

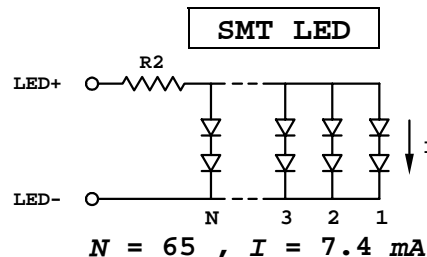
BACKLIGHT

-BEXX -- EL -
 BLSXX -- LED SMT
 -BFEXX -- CCFT EDGE
 -BFSXX -- CCFT DIRECT

INPUT VOLTAGE & CURRENT

100 V_{RMS} (400-800) Hz; 14.0mA
 + 5V DC; 480 mA R2= 2.2 Ohm 1 W
 235 V_{RMS} 4.0 mA
 250 V_{RMS} 6.0 mA

*R2:Suggest BL current limit resistor on customer board



N = 65 , I = 7.4 mA

----- **Single +5V Operation** -----

SINGLE +5V OPERATION only

TEMPERATURE COMPENSATION

-VNV
 -- V_{EE} self-generate --

-VNVTC (-VNT)
 -- V_{DD}-V_O self-generate & compensated --

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APPLICABLE FOR -LNY

	<u>FSTN</u>		<u>STN</u>	
	<u>TEMPERATURE</u>		<u>TEMPERATURE</u>	
	<u>NORMAL</u>	<u>NORMAL</u>	<u>NORMAL</u>	<u>WIDE</u>
Supply Current, (I_{DD})Typ., mA	12	12	12	12
Supply Current, (I_{EE})Typ., mA	1	1	1	1
Supply Voltage, (V_{EE})Typ., V	-10	-10	-10	-10
Recommend LCD drive Voltage:				
$(V_{DD}-V_O)$ at $T_a = -20^{\circ}\text{C}$, Volts	N/A	N/A	N/A	13.5
$T_a = 0^{\circ}\text{C}$	14.3	14.9	14.3	13.3
$T_a = 25^{\circ}\text{C}$	13.2	13.7	13.5	13.0
$T_a = 50^{\circ}\text{C}$	12.4	12.4	12.8	12.8
$T_a = 70^{\circ}\text{C}$	N/A	N/A	N/A	12.6

ABSOLUTE MAXIMUM RATINGS

	<u>NORMAL</u>		<u>WIDE TEMPERATURE</u>	
	<u>MIN</u>	<u>MAX</u>	<u>MIN</u>	<u>MAX</u>
Input Voltage (V_I) V	0	V_{DD}	0	V_{DD}
Supply for Logic ($V_{DD}-V_{SS}$) V	0	7	0	7
Supply for LCD ($V_{DD}-V_O$) V	0	23	0	23
Operating Temperature T_{OP} , $^{\circ}\text{C}$	0	+50	-20	+70
Storage Temperature T_{ST} , $^{\circ}\text{C}$	-20	+70	-30	+80

OPTION

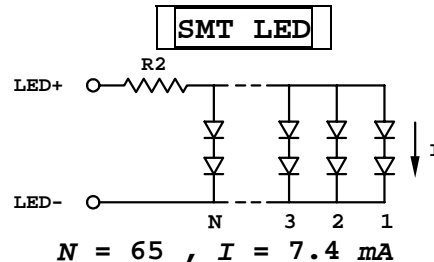
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